

Specifications

This section contains the specifications for the TCS101 product.

Specification Table

Table 4–1 lists the electrical requirements that the target system must produce for the support to acquire correct data.

Table 4–1: Electrical specifications

Characteristics	Requirements
Target system clock rate TCS101 specified clock rate for SPI3_TX and SPI3_RX support packages	The maximum rates are 120, 200, 235, 450 MHz depending upon the type of logic analyzer module
TCS101 specified clock rate for SPI4 and SPI4_LVTTL support packages	Maximum 350 MHz ¹
Minimum setup time required for the Logic analyzer TLA7Axx	0.750 ns
Minimum hold time required for the Logic analyzer TLA7Axx	0 ns
Minimum setup time required for the Logic analyzers TLA7xx, TLA6xx	2.5 ns
Minimum hold time required for the Logic analyzer TLA7xx, TLA6xx	0 ns

¹ **Specification at time of printing. Contact your Tektronix sales representative for current information on the fastest bus supported.**